

< IGBT MODULES >

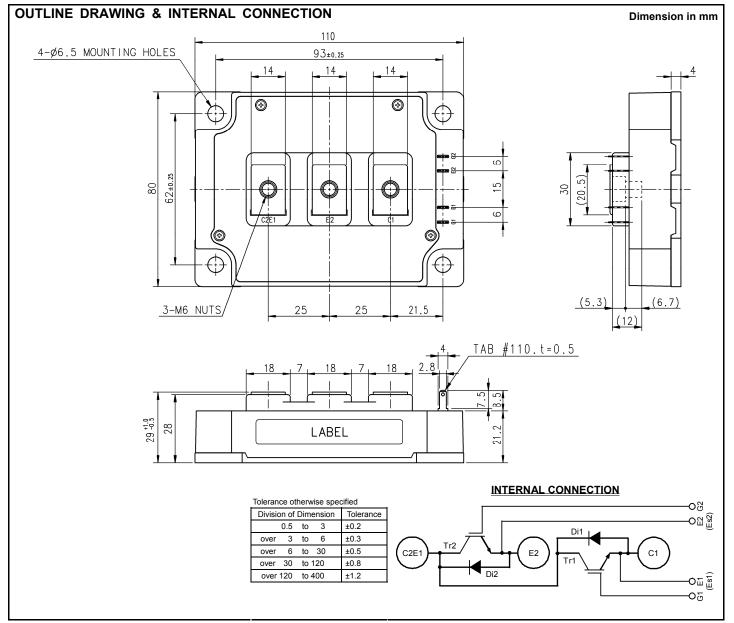
CM400C1Y-24S

HIGH POWER SWITCHING USE INSULATED TYPE

	Collector current I _C 3 5 0 A*
	Collector-emitter voltage V _{CES} 1 2 0 0 V
	Maximum junction temperature T _{jmax} 175°C
	●Flat base Type
	Copper base plate
	RoHS Directive compliance
	 UL Recognized under UL1557, File E323585
Dual (AC switch)	*. DC current rating is limited by power terminals.

APPLICATION

AC Power Switch for NPC



ABSOLUTE MAXIMUM RATINGS (Tj=25 °C, unless otherwise specified)

Symbol	Item Conditions		Rating	Unit	
V _{CES}	Collector-emitter voltage	G-E short-circuited	1200	V	
V_{GES}	Gate-emitter voltage	C-E short-circuited	±20	V	
Ic	Collector ourrent	DC, T _C =124 °C (Note.2, 4)	350 *	•	
I _{CRM}	Collector current	Pulse, Repetitive (Note.3)	800	A	
P _{tot}	Total power dissipation	T _C =25 °C (Note.2, 4)	2670	W	
IE (Note.1)	Emitter current	T _C =25 °C (Note.2, 4)	350 *	•	
IERM (Note.1)		Pulse, Repetitive (Note.3)	800	A	
Visol	Isolation voltage	Terminals to base plate, RMS, f=60 Hz, AC 1 min	2500	V	
T _{jmax}	Maximum junction temperature	-	175	°C	
T _{cmax}	Maximum case temperature	(Note.2)	125		
T _{jopr}	Operating junction temperature	-	-40 ~ +150	°C	
T _{stg}	Storage temperature	-	-40 ~ +125		

ELECTRICAL CHARACTERISTICS (T_j=25 °C, unless otherwise specified)

Symbol	Item	Conditions	Conditions		Limits		
Symbol	item	Conditions		Min.	Тур.	Max.	Unit
I _{CES}	Collector-emitter cut-off current	V _{CE} =V _{CES} , G-E short-circuited		-	-	1.0	mA
I _{GES}	Gate-emitter leakage current	V _{GE} =V _{GES} , C-E short-circuited		-	-	0.5	μA
$V_{\text{GE(th)}}$	Gate-emitter threshold voltage	I _C =40 mA, V _{CE} =10 V		5.4	6.0	6.6	V
		I _C =400 A ^(Note.5) ,	T _j =25 °C	-	1.85	2.30	
		V _{GE} =15 V,	T _j =125 °C	-	2.05	-	V
M	Collector-emitter saturation voltage	Terminal	T _j =150 °C	-	2.10	-	
V_{CEsat}	Collector-enlitter saturation voltage	I _C =400 A ^(Note.5) ,	Tj=25 °C	-	1.70	2.15	
		V _{GE} =15 V,	T _j =125 °C	-	1.90	-	V
		Chip	T _j =150 °C	-	1.95	-	
Cies	Input capacitance	V _{cE} =10 V, G-E short-circuited		-	-	40	
C _{oes}	Output capacitance			-	-	8.0	nF
Cres	Reverse transfer capacitance		-	-	0.66		
Q _G	Gate charge	V _{cc} =600 V, I _c =400 A, V _{GE} =15 V		-	934	-	nC
t _{d(on)}	Turn-on delay time	- V _{cc} =600 V, I _c =400 A, V _{GE} =±15 V, R _G =0 Ω, Inductive load		-	-	800	
tr	Rise time			-	-	200	
$t_{d(off)}$	Turn-off delay time			-	-	600	ns
t _f	Fall time			-	-	300	
		I _E =400 A ^(Note.5) ,	T _j =25 °C	-	1.85	2.30	
		G-E short-circuited,	T _j =125 °C	-	1.85	-	V
V _{EC} (Note.1)	Emitter-collector voltage	Terminal	T _j =150 °C	-	1.85	-	
V _{EC}		I _E =400 A ^(Note.5) ,	T _j =25 °C	-	1.70	2.15	
		G-E short-circuited,	T _j =125 °C	-	1.70	-	V
		Chip	T _j =150 °C	-	1.70	-	
t _{rr} ^(Note.1)	Reverse recovery time	V _{CC} =600 V, I _E =400 A, V _{GE} =±15 V,		-	-	300	ns
Q _{rr} (Note.1)	Reverse recovery charge	$R_{G}=0 \Omega$, Inductive load		-	21.4	-	μC
Eon	Turn-on switching energy per pulse	V _{CC} =600 V, I _C =I _E =400 A,		-	39.8	-	
E _{off}	Turn-off switching energy per pulse	V_{GE} =±15 V, R _G =0 Ω,		-	44.9	-	mJ
Err (Note.1)	Reverse recovery energy per pulse	T _i =150 °C, Inductive load		-	35.2	-	mJ
r _g	Internal gate resistance	Per switch		-	4.9	-	Ω

THERMAL RESISTANCE CHARACTERISTICS

Symbol Item	Itom	Conditions	Limits			Unit
	Conditions	Min.	Тур.	Max.	Unit	
R _{th(j-c)Q}	Thermal resistance (Note.2)	Junction to case, per IGBT	-	-	56	K/kW
R _{th(j-c)D}		Junction to case, per FWDi	-	-	95	K/kW
R _{th(c-s)}	Contact thermal resistance (Note.2)	Case to heat sink, per 1/2 module, Thermal grease applied ^(Note.6)	-	18	-	K/kW

MECHANICAL CHARACTERISTICS

Symbol	Item	Conditions		Limits			Unit
Symbol	Symbol			Min.	Тур.	Max.	Offic
Mt	Mounting torque	Main terminals	M 6 screw	3.5	4.0	4.5	N∙m
Ms		Mounting to heat sink	M 6 screw	3.5	4.0	4.5	N∙m
m	Weight	-		-	580	-	g
e _c	Flatness of base plate	On the centerline X, Y (Note.7)		-100	-	+100	μm

Note1. Represent ratings and characteristics of the anti-parallel, emitter-collector free wheeling diode (FWDi).

2. Case temperature (T_c) and heat sink temperature (T_s) are defined on the each surface (mounting side) of base plate and heat sink just under the chips. Refer to the figure of chip location.

The heat sink thermal resistance should measure just under the chips.

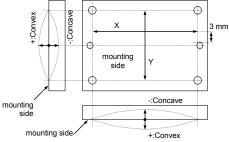
3. Pulse width and repetition rate should be such that the device junction temperature (T_j) dose not exceed T_{jmax} rating.

4. Junction temperature (T_j) should not increase beyond T_{jmax} rating.

5. Pulse width and repetition rate should be such as to cause negligible temperature rise. Refer to the figure of test circuit.

6. Typical value is measured by using thermally conductive grease of λ =0.9 W/(m·K).

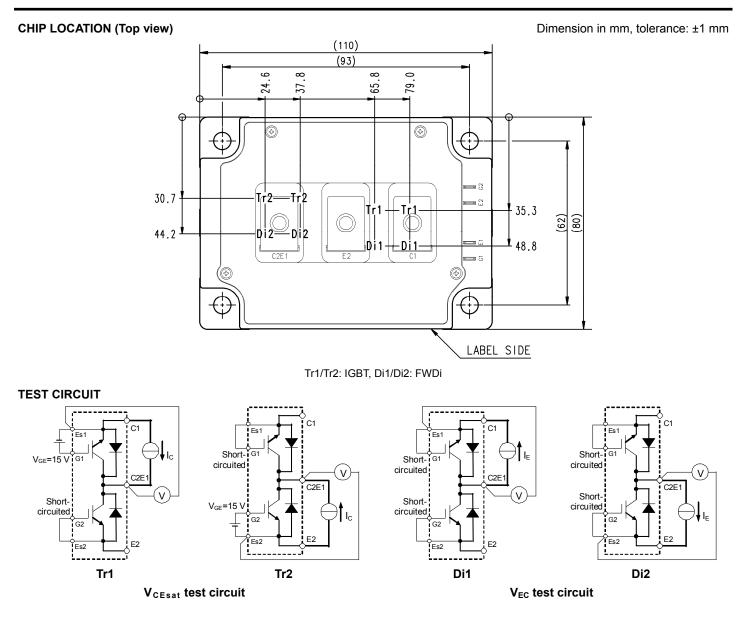
7. Base plate (mounting side) flatness measurement points (X, Y) are as follows of the following figure.



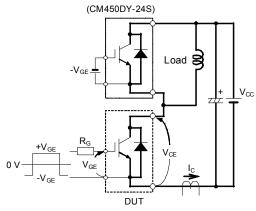
*. DC current rating is limited by power terminals.

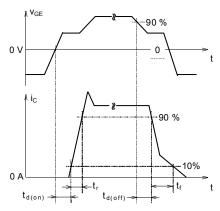
RECOMMENDED OPERATING CONDITIONS

Symbol	Item	Conditions	Limits			Unit
	item		Min.	Тур.	Max.	Unit
Vcc	(DC) Supply voltage	Applied across C1-E2	-	600	850	V
V _{GEon}	Gate (-emitter drive) voltage	Applied across G1-Es1/G2-Es2	13.5	15.0	16.5	V
R _G	External gate resistance	Per switch	0	-	10	Ω

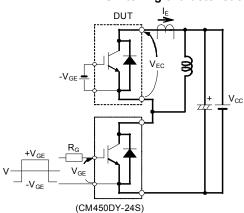


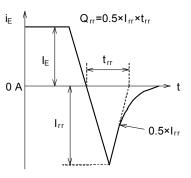
TEST CIRCUIT AND WAVEFORMS



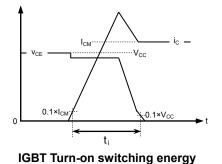


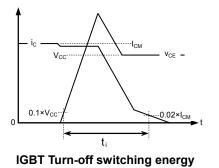
Switching characteristics test circuit and waveform

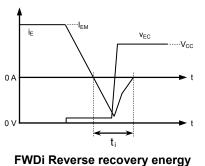




Reverse recovery characteristics (trr, Qrr) test circuit

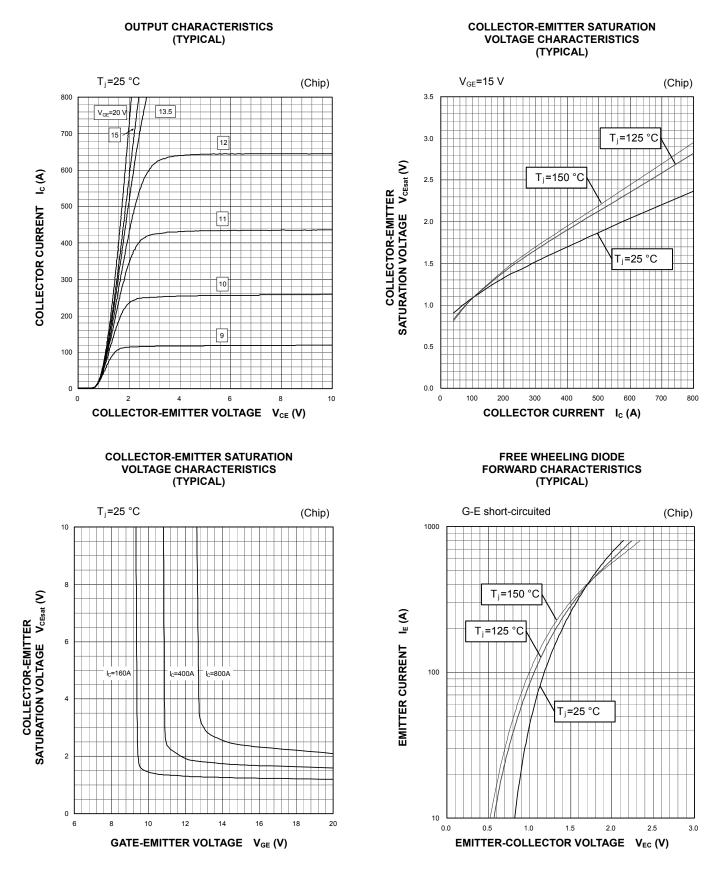




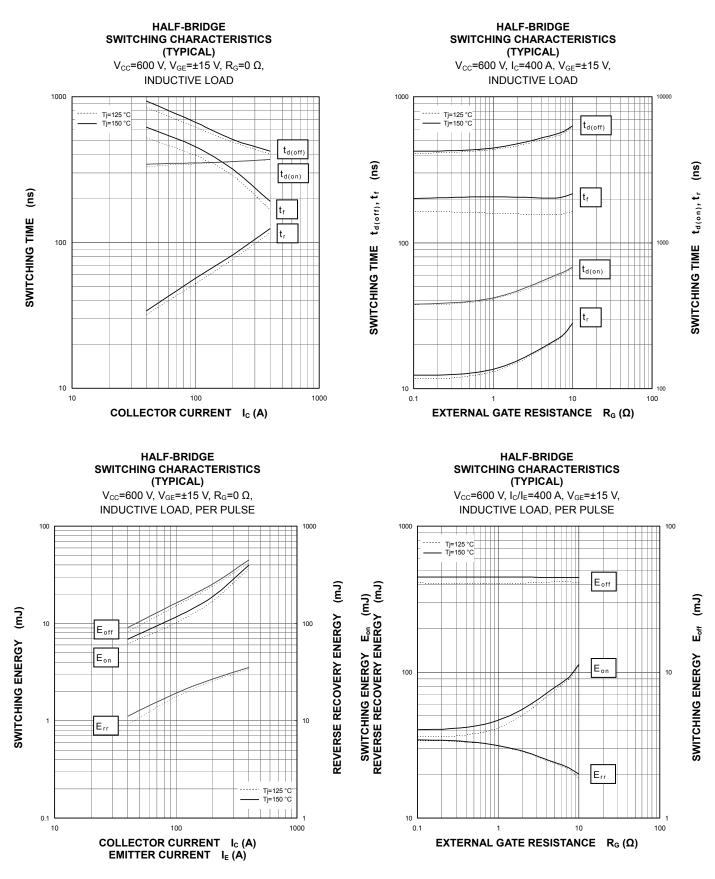


Turn-on / Turn-off switching energy and Reverse recovery energy test waveforms (Integral time instruction drawing)

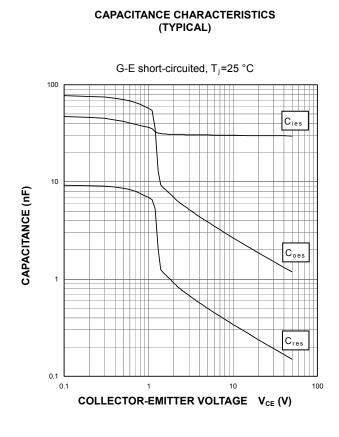
PERFORMANCE CURVES



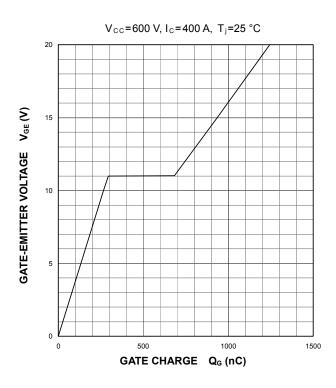
PERFORMANCE CURVES

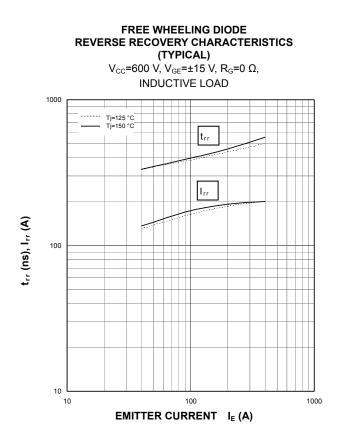


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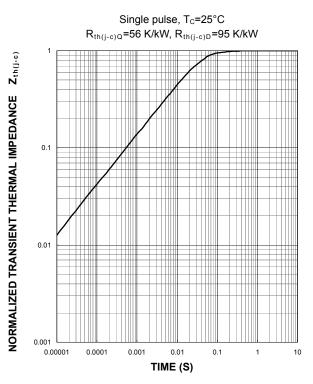


GATE CHARGE CHARACTERISTICS (TYPICAL)





TRANSIENT THERMAL IMPEDANCE CHARACTERISTICS (MAXIMUM)



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